

Refine Search

Search Results -

Terms	Documents
((("low pin count" or LPC) near5 memory) same ((firmware or BIOS) near5 memory))	28

Database:

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search:

L1

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Thursday, December 29, 2005 [Printable Copy](#) [Create Case](#)

Set Name Query
side by side

Hit Count Set Name
result set

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L1 (("low pin count" or LPC) near5 memory) same ((firmware or BIOS) near5 memory) 28 L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1 and L3	15

Database:

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search:

L4

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Thursday, December 29, 2005 [Printable Copy](#) [Create Case](#)

Set
Name Query
side by
side

J
C

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L4 11 and L3

L3 710/305,33,74,1,315,316;709/228,230,250;711/149,100,145,211;713/1,100;704/7;365/63,189.08,230.05.ccls. 17

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L2 L1

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L1 (("low pin count" or LPC) near5 memory) same ((firmware or BIOS) near5 memory)

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1	0

Database:

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
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Search:

L2

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Thursday, December 29, 2005 [Printable Copy](#) [Create Case](#)

Set Name	Query	Hit Count	Set Name
side by side			result set
DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L2	L1	0	L2
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR			
L1	("low pin count" or LPC) near5 memory) same ((firmware or BIOS) near5 memory)	28	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
"1-bit" near5 "memory flag"	3

Database:

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
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- Derwent World Patents Index
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Search:

L2

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Thursday, December 29, 2005 [Printable Copy](#) [Create Case](#)

<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
side by side			result set
<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>			
<u>L2</u>	"1-bit" near5 "memory flag"	3	<u>L2</u>
<u>L1</u>	"1-bit memory flag"	0	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
"1-bit" near5 "memory flag"	1

Database:

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
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Search:

L3

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Thursday, December 29, 2005 [Printable Copy](#) [Create Case](#)

<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
side by side			result set
<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L3</u>	"1-bit" near5 "memory flag"	1	<u>L3</u>
<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>			
<u>L2</u>	"1-bit" near5 "memory flag"	3	<u>L2</u>
<u>L1</u>	"1-bit memory flag"	0	<u>L1</u>

END OF SEARCH HISTORY



Welcome United States Patent and Trademark Office

Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPORT

Results for "((low pin count) or lpc<in>metadata) and (firmware or bios)"

Your search matched 7 of 1286976 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

e-mail printer friendly

Search Options

[View Session History](#)

[New Search](#)

Modify Search

((low pin count) or lpc<in>metadata) and (firmware or bios)

>>

☐ Check to search only within this results set

Display Format: ☒ Citation ☐ Citation & Abstract

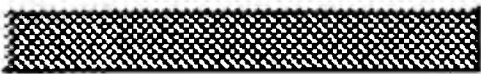
Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Select Article Information

- ☐
1. **Voice Coding and Tree Encoding Speech Compression Systems Based Upon Inverse Filter Matching**
Matsuyama, Y.; Gray, R.;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 30, Issue 4, Apr 1982 Page(s):711 - 720
[AbstractPlus](#) | Full Text: [PDF](#)(1056 KB) [IEEE JNL](#)
- ☐
2. **Implementation of a Two-Channel LPC Vocoder**
Byoung Shin; Hyeong Lee; Chong Un; Sang Lee; Kil Song;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 31, Issue 7, Jul 1983 Page(s):907 - 916
[AbstractPlus](#) | Full Text: [PDF](#)(824 KB) [IEEE JNL](#)
- ☐
3. **Implementation of a Multirate Speech Digitizer**
Byung Lee; Chong Un; Hyeong Lee; Byoung Shin; Hwang Lee;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 31, Issue 6, Jun 1983 Page(s):775 - 783
[AbstractPlus](#) | Full Text: [PDF](#)(928 KB) [IEEE JNL](#)
- ☐
4. **The Impact of Mpact 2**
Purcell, S.;
Signal Processing Magazine, IEEE
Volume 15, Issue 2, March 1998 Page(s):102 - 107
Digital Object Identifier 10.1109/79.664703
[AbstractPlus](#) | Full Text: [PDF](#)(612 KB) [IEEE JNL](#)
- ☐
5. **A 2000-MOPS embedded RISC processor with a Rambus DRAM controller**
Suzuki, K.; Daito, M.; Inoue, T.; Nadehara, K.; Nomura, M.; Mizuno, M.; Iima, T.; Sato, S.; Fukuda, T.; Arai, T.; Kuroda, I.; Yamashina, M.;
Solid-State Circuits, IEEE Journal of
Volume 34, Issue 7, July 1999 Page(s):1010 - 1021
Digital Object Identifier 10.1109/4.772417
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(512 KB) [IEEE JNL](#)
- ☐
6. **IEEE standard for high-bandwidth memory Interface based on Scalable Coherent Interface (SCI) signaling technology (RamLink)**
IEEE Std 1596.4-1996
15 Sept. 1996
[AbstractPlus](#) | Full Text: [PDF](#)(968 KB) [IEEE STD](#)
- ☐
7. **IEEE standard for scalable coherent interface (SCI).**
IEEE Std 1596-1992

2 August 1993 Page(s):i
AbstractPlus | Full Text: PDF(5684 KB) IEEE STD



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